



Faculty of Science



Reversible Multi-head Finite Automata Characterize Reversible Logarithmic Space

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Overview

- Motivation
- Informal acronyms: RMFAs and RTMs
- Simulating a logspace RTM with an RMFA
 - Encoding of configurations
 - Simulation of transitions
 - Read/write procedures
- Summing up



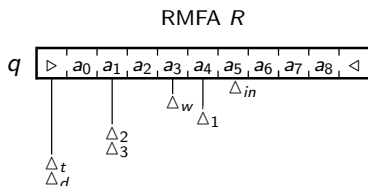
Motivation

Why this characterization?

- Recent open problem stated by Morita
 - Reversible PDAs are *weaker* than DPDAs
 - Reversible LBAs are *equivalent* to DLBAs
 - What about automata between these?
- $\mathcal{L}(\text{RMFA})$ is strong: palindromes, unary primes, DYCK(1)
- (D)MFAs characterize logspace very nicely
 - Logspace = constant amount of pointers
- Usual reversibilization trick won't work for RMFAs
 - No space for a history, or other *garbage data*
 - What to do if we *cannot* write at all?
- Nice use case for techniques from recent RTM work



Reversible Multi-head Finite Automata

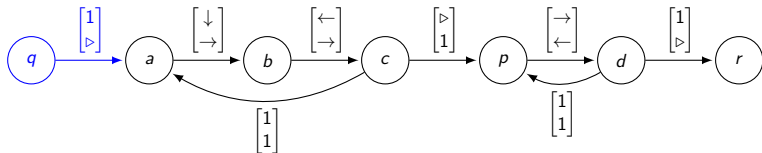
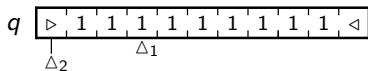


- Input word $w \in \Sigma^*$ in delimited form $\triangleright w \triangleleft$
- k heads (all start by pointing at \triangleright)
- Triple format transitions
 - symbol rules: $(q, [a, b, \triangleright, \dots, c], p)$
 - move rules: $(q, [\leftarrow, \downarrow, \leftarrow, \dots, \rightarrow], p)$
- Reversibility = forward + backward determinism
 - Fwd determinism: (q, s, \cdot) and (q, t, \cdot) imply $s, t \in \Sigma^k, s \neq t$
 - Bwd determinism: (\cdot, s, p) and (\cdot, t, p) imply $s, t \in \Sigma^k, s \neq t$



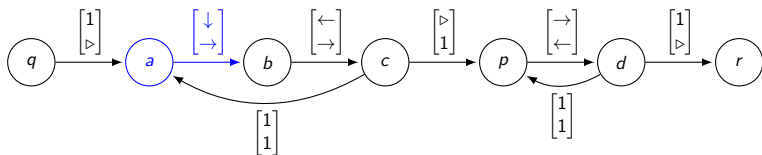
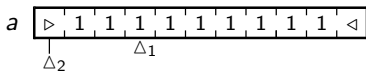
RMFA example

Doubling the unary counter in Δ_1 :



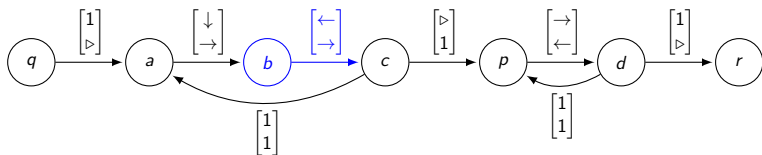
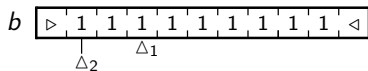
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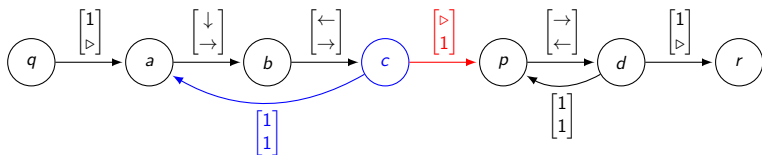
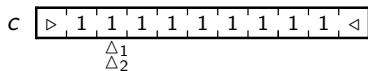
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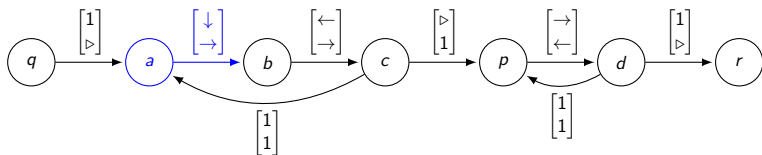
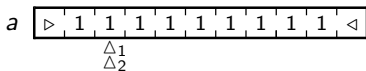
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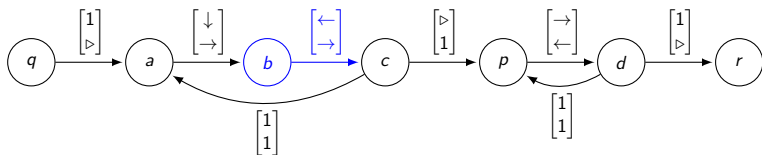
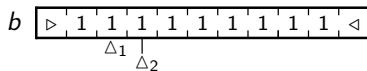
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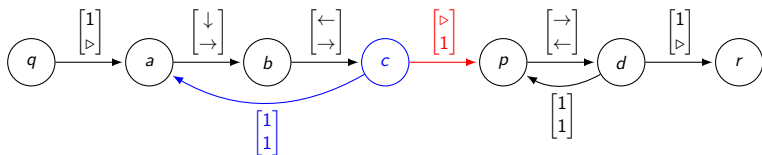
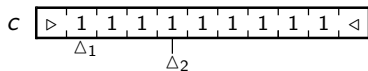
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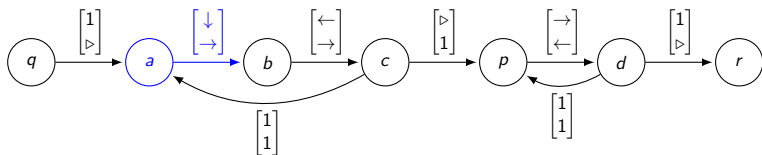
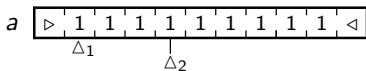
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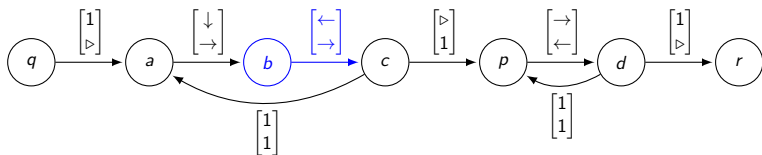
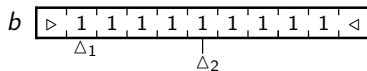
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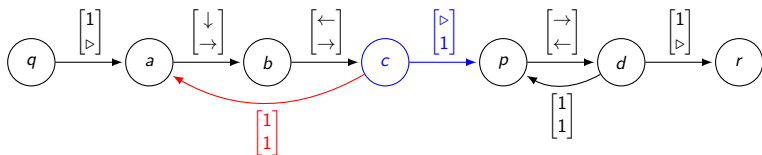
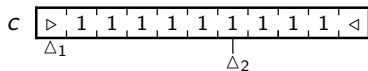
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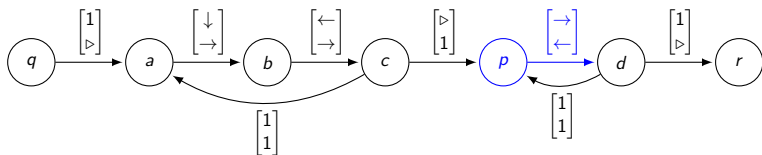
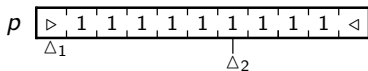
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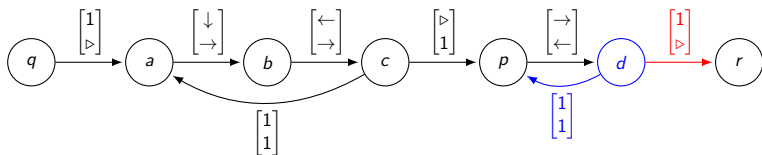
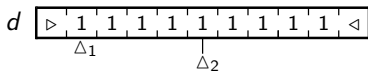
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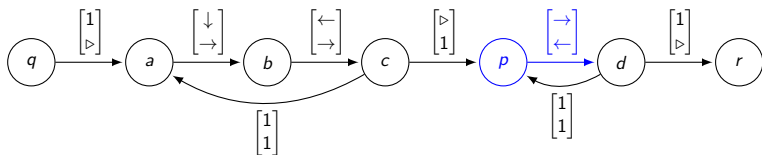
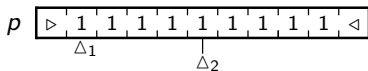
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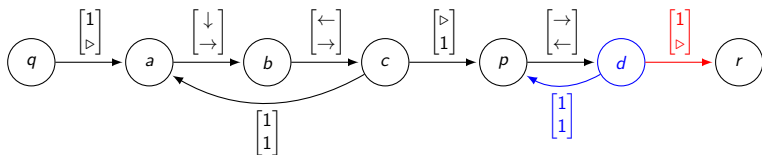
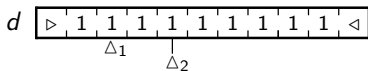
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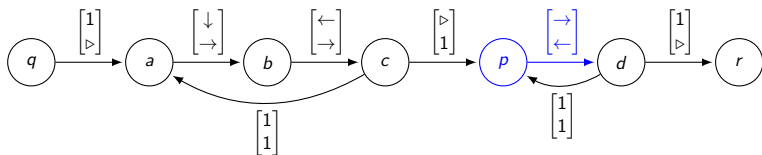
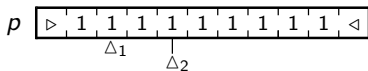
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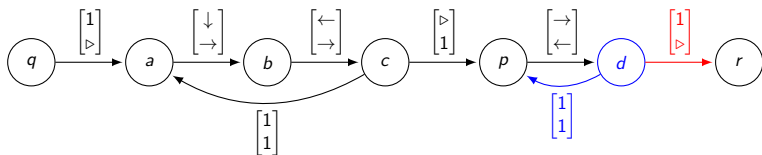
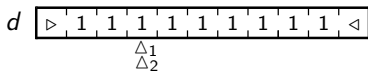
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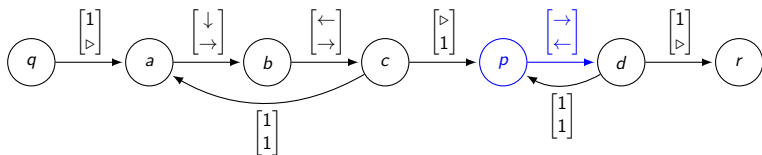
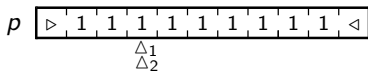
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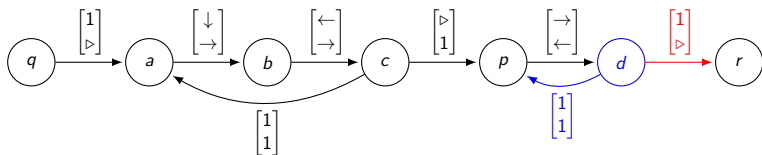
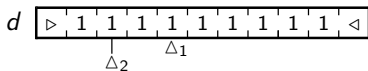
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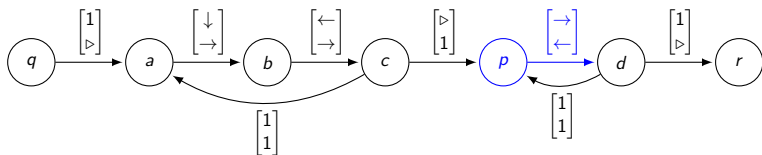
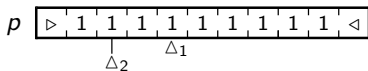
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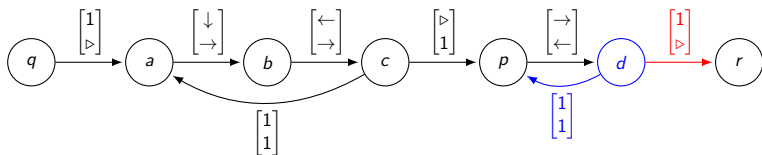
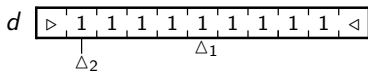
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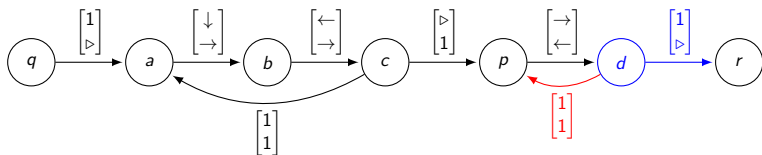
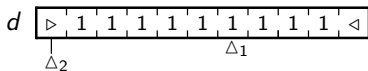
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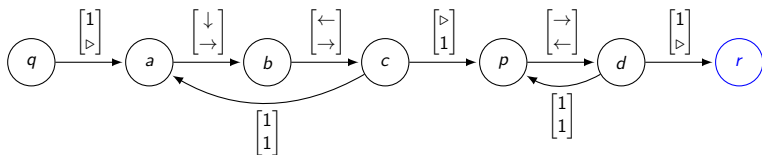
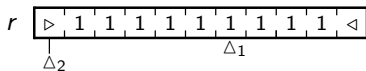
RMFA example

Doubling the unary counter in Δ_1



RMFA example

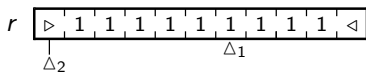
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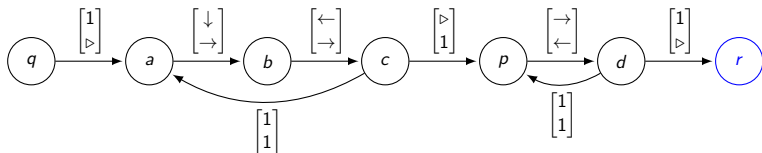
RMFA example: inversion

RMFAs are *invertible*: swap direction of transitions, swap \leftarrow and \rightarrow

Doubling the unary counter in Δ_1



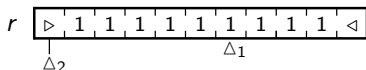
Invert:



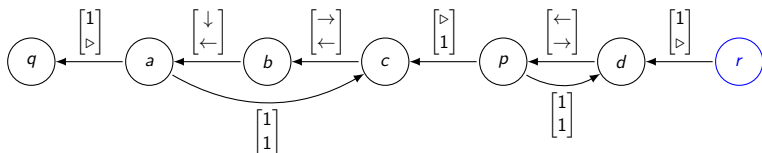
RMFA example: inversion

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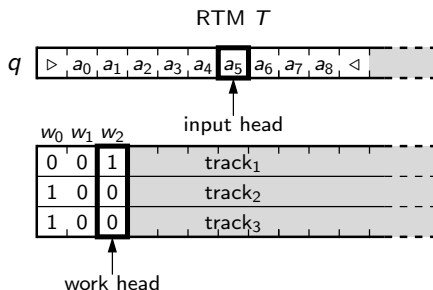
Invert:



This is an RMFA for **halving** the unary counter in Δ_1



Reversible Turing Machines



- Input tape as before, but only **one input head**.
- Read/write **work tape**: one head, k binary tracks.
 - **Space usage** measured on the work tape.
- Triple format transitions, but symbol rules can now **write** on the work tape: $(q, [a, 100 \mapsto 011], p)$.
- **Reversibility** as before.



What do we know & what shall we prove

 $\mathcal{L}(\text{RMFA})$
Morita
 $\mathcal{L}(\text{DMFA})$

|| Here

|| Hartmanis'72

 $\text{RevSPACE}(\log n)$
Lange et al.'00
 $\text{DSPACE}(\log n)$


Proving $\mathcal{L}(\text{RMFA}) = \text{RevSPACE}(\log n)$

Will show $\text{RevSPACE}(\log n) \subseteq \mathcal{L}(\text{RMFA})$.

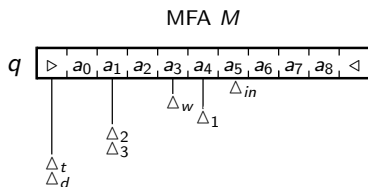
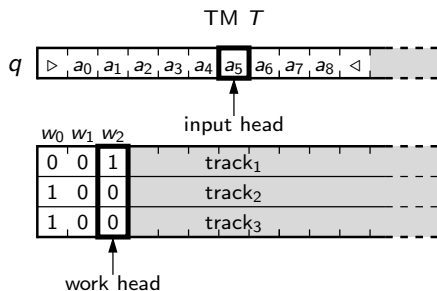
Proof: Simulate a logspace RTM with an RMFA.

- Encoding of RTM configurations
- Transition simulation
- Tape read/write simulation



Encoding logspace TM configurations in MFAs

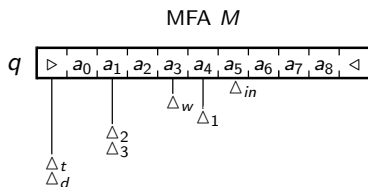
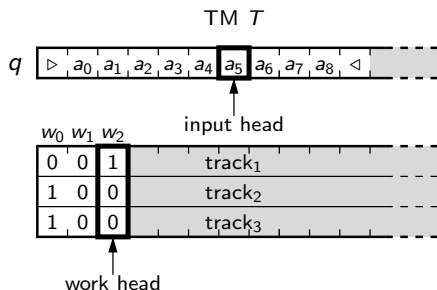
TMs use k binary tracks, strictly $\log n$ tape bounded.



- State q (in the TM) maps to q (in the MFA)
- \triangle_{in} mirrors *input head* position
- \triangle_w is $2^{\text{work head position}}$ places from \triangleright
- $\triangle_1, \triangle_2, \triangle_3, \dots, \triangle_k$ simulates work tape *content* by track
- \triangle_t, \triangle_d are ancillary heads for book-keeping



Encoding track content by head position



Track encoding: Δ_i points at a_n means track _{i} contains $(\text{bin}(n))^R$:

- track₁ contains '001', in reverse binary $100 = 4$,
- so Δ_1 points at a_4 , encoding 4 in unary.

To read work cell w_2 we must consult all of $\Delta_1, \Delta_2, \Delta_3$



Simulating RTM transitions

By determinism, the transitions going out of source state q are

- all **symbol rules**, or
- a single **move rule**.

We'll tackle these in turn.



Simulating symbol transitions $(q, [\cdot, \cdot \mapsto \cdot], \cdot)$

To simulate a symbol transition $(q, [\cdot, \cdot \mapsto \cdot], \cdot)$ we need

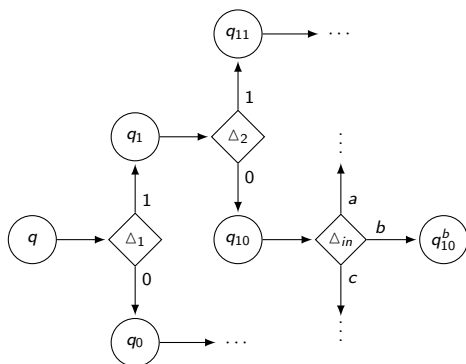
- to read the input tape symbol (easy)
- to read the work tape symbol (not so easy)
- to write a new work tape symbol (easy)
- to go into target state (not so easy)

Work tape symbol is encoded across all track heads by their *position alone*, not the local symbol.

Use a *staged reading* of the symbols, store in the state.



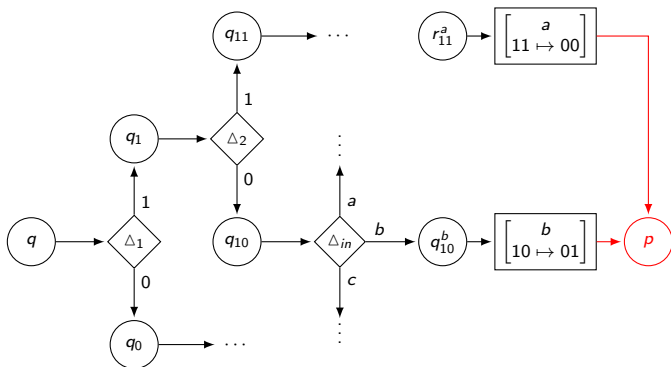
Simulating RTM symbol rules $(q, [\cdot, \mapsto \cdot], \cdot)$



(Assuming we have a read procedure for track heads Δ_i .)



Simulating RTM symbol rules $(q, [b, 10 \mapsto 01], p)$

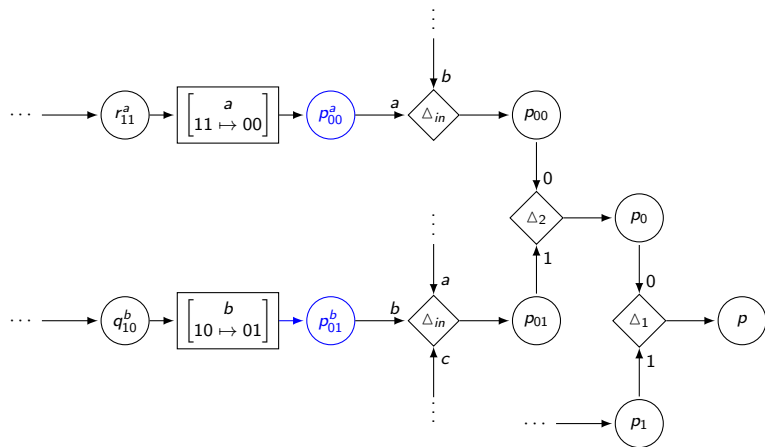


Doesn't work: there can be *other* symbol transitions targeting p , say, $(r, [a, 11 \mapsto 00], p)$. Direct links to p **break reversibility**.

Solution: Exploit reversibility of the RTM. *No other transition targeting p can write both b and 01 to the tapes.*



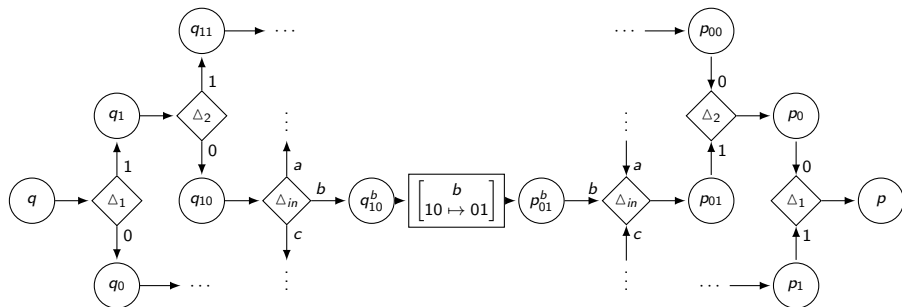
Simulating RTM symbol rules $(\cdot, [\cdot, \cdot \mapsto \cdot], p)$



Intuition: Link up the appropriate leaves of decision trees of *source* states (r, q) with leaves of *inverse* decision tree for *target* state p .



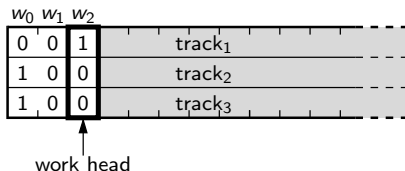
Simulating RTM symbol rules $(q, [b, 10 \mapsto 01], p)$



Subpart of symbol rule simulation for $(q, [b, 10 \mapsto 01], p)$.



Reading procedure



1. Each track head encodes just 1 bit from each work tape cell.
2. If the RTM work head points to cell w_p , then the RMFA work head Δ_w is 2^p cells to the right of \triangleright .

Idea: The p th bit of n in binary is

$$(n \operatorname{div} 2^p) \bmod 2.$$



Reading procedure 2

Idea: The p th bit of n in binary is

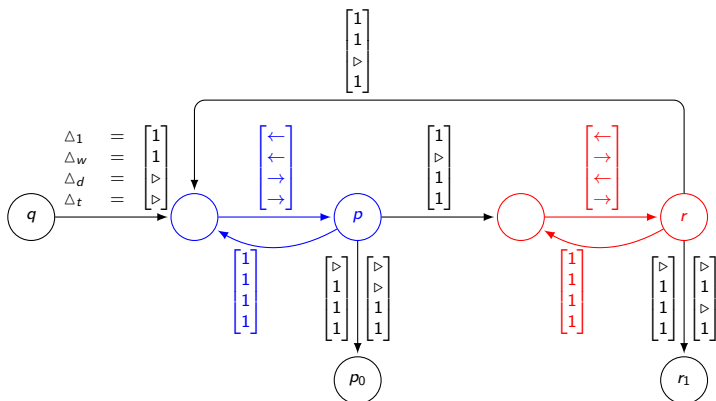
$$(n \operatorname{div} 2^p) \bmod 2.$$

most sign.	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	0	0	0	0	1	1	1	1	0	0	0	0	1	1	1	1
	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1
least sign.	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1

Reading procedure: Read off p th bit from track i , by repeatedly 'subtracting' 2^p (Δ_w 's offset) from Δ_i . Δ_i 's original position is conserved in Δ_t , and the original position of Δ_w in Δ_d .



Reversible reading procedure for Δ_1

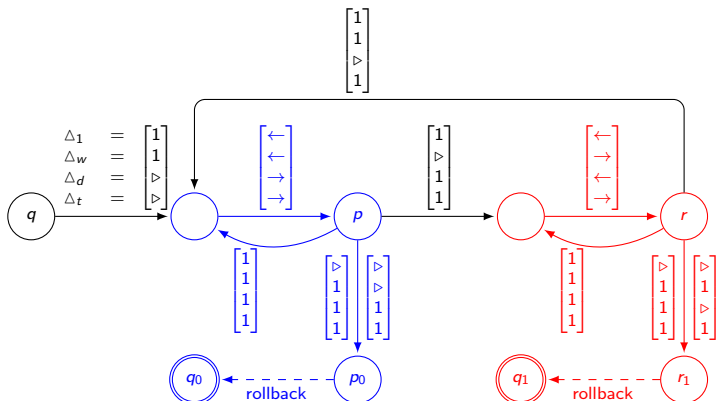


Blue loop: subtract Δ_w from Δ_1 , believing the bit is 0.

Red loop: subtract Δ_w from Δ_1 , believing the bit is 1.



Reversible reading procedure for Δ_1



Problem: head positions are not conserved.

Solution: *rollbacks* are *inverse* copies of the reading procedure, restoring all heads to their previous positions, *reversibly*!



Reversible writing procedure

An RTM symbol rule such as $(q, (a, 1010 \mapsto 0011), p)$ define *explicit* bit flips. Flipping the p th bit encoded on track i is simple:

- $0 \mapsto 1$: move $\Delta_i 2^p$ positions to the *right*
- $1 \mapsto 0$: move $\Delta_i 2^p$ positions to the *left*

Example: Let Δ_2 point at a_{10} , which encodes 1010. To flip the 3rd bit from 0 to 1 we move $\Delta_2 2^2$ right. It then points at a_{14} , encoding 1110.

Although overwriting symbols *in general* is irreversible, overwriting a *specific* known symbol is not.



Simulate move rules

Simulating an RTM move $(q, [\leftarrow, \rightarrow], p)$ is considerably easier.

The RMFA does as follows.

- From q ,
- Δ_{in} mirrors input head,
- if work tape head moves:
 - right, then *double* Δ_w
 - left, then *halve* Δ_w (with inverse doubling procedure)
- go into state p .

Note: We do *not* have to worry about about other transitions targeting p , because by reversibility there *are no others!*



Summary

We now have:

- Encoding from RTM to RMFA configuration
- Simulation of RTM transitions in the RMFA

So we're done with showing $\text{RevSPACE}(\log n) \subseteq \mathcal{L}(\text{RMFA})$.

The reverse inclusion, $\mathcal{L}(\text{RMFA}) \subseteq \text{RevSPACE}(\log n)$ is considerably easier, so

$$\text{RevSPACE}(\log n) = \mathcal{L}(\text{RMFA}).$$

Future work: Other reversible automata models, 2-way vs. 1-way.



www.reversible-computation.org

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Deadline extended:
- **Abstract Submission: March 9th, 2012**
- **Submission Deadline: March 16th, 2012**

The Workshop on Reversible Computation will bring together researchers from computer science, mathematics, engineering, and physics to discuss new developments and directions for future research in the emerging area of Reversible Computation. This particularly includes applications of reversibility in quantum computation. Research papers, tutorials, tool demonstrations, and work-in-progress reports are within the scope of the workshop.

The 4th Workshop on Reversible Computation will take place on July 2nd and 3rd, 2012 in Copenhagen, Denmark and is organized by the University of Copenhagen. Accepted papers will appear in the preliminary proceedings at the workshop. Furthermore, authors of selected papers will be invited after the workshop to prepare a final version of their paper to be published in Springer's Lecture Notes in Computer Science (LNCS).

The Workshop will be held in parallel with the European Conference on Modelling Foundations and Applications.



Important Dates:

Abstract Submission:
Fri, March 9th, 2012

Submission Deadline:
Fri, March 16th, 2012

Notification to Author:
Mon, May 7th, 2012

Final Version:
Fri, June 8th, 2012

Workshop:
Mon-Tue July 2nd-3rd, 2012

Workshop Organizer

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